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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Kazuhiko OKAWA et al.

Group Art Unit: 5674

Application No.: 09/866,800

Examiner: J. P. Mondt

Filed: May 30, 2001

Docket No.: 109657

For: SEMICONDUCTOR DEVICE HAVING ELECTROSTATIC
PROTECTION CIRCUIT AND METHOD OF FABRICATING THE SAME

AMENDMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

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TECHNOLOGY CENTER 2800

In reply to the October 8, 2002 Office Action, the shortened statutory period for reply being extended by the Attached Petition for Extension of Time, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 8-19 and 24 without prejudice to or disclaimer of the subject matter contained therein. Applicants reserve the right to file a Divisional Application to pursue the claims.

Please replace claims 1 and 20 as follows:

1. (Twice Amended) A semiconductor device comprising:
a semiconductor substrate;
a MOS transistor which is formed on the semiconductor substrate and includes a first diffusion region;

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